

BEST AVAILABLE COPY

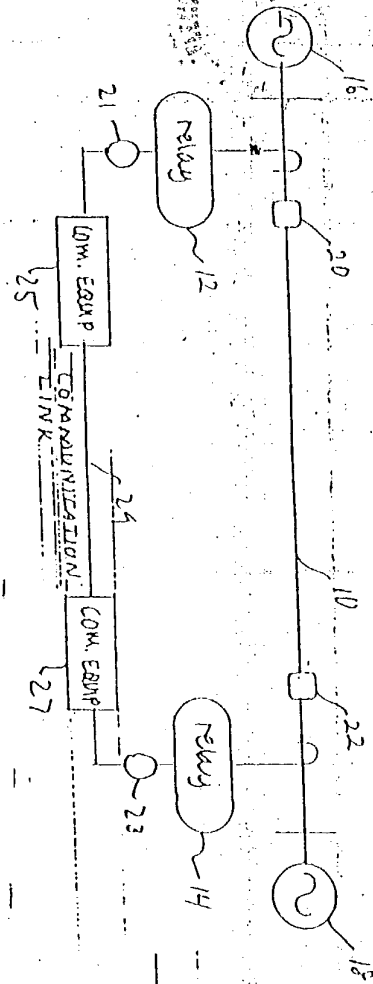


FIG. 1

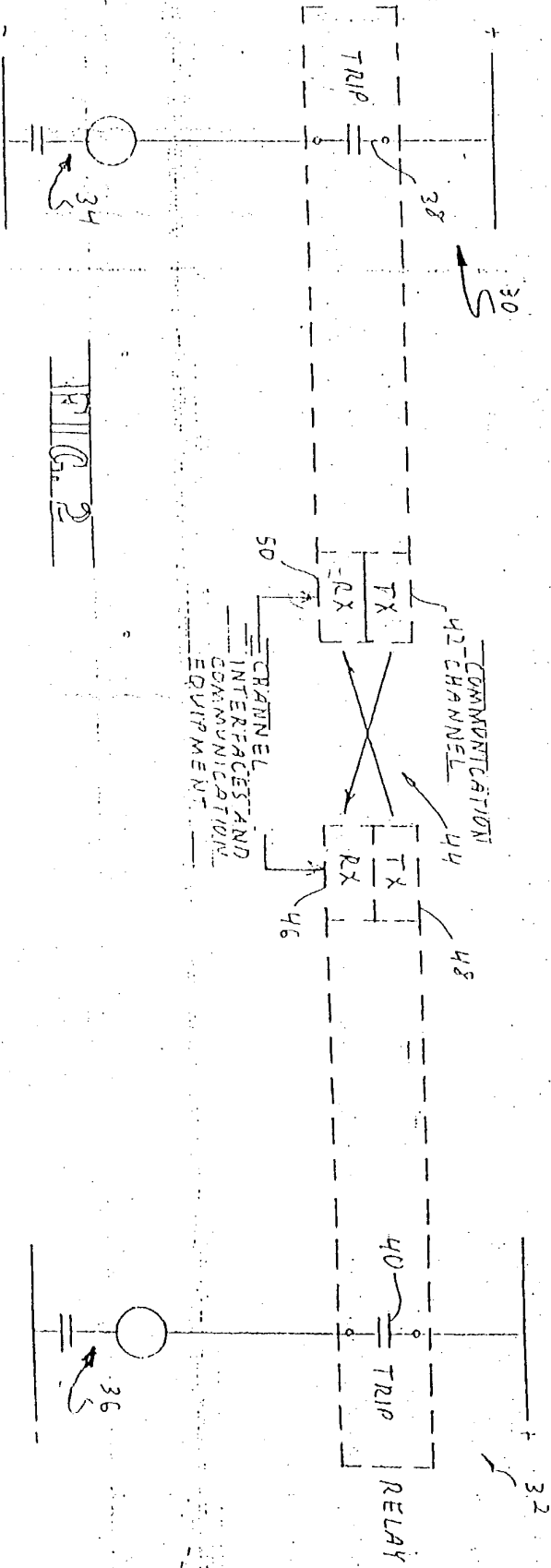
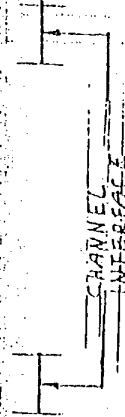
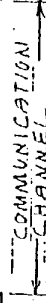
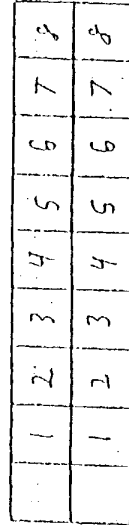
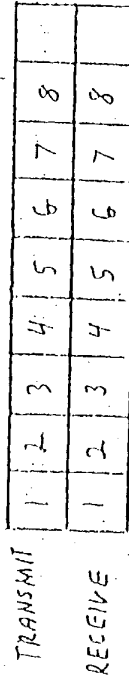
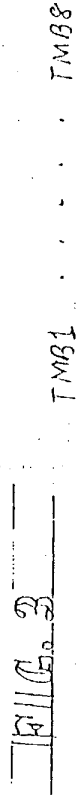
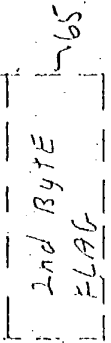
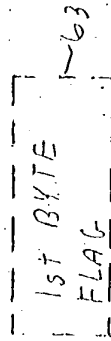
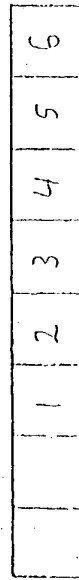
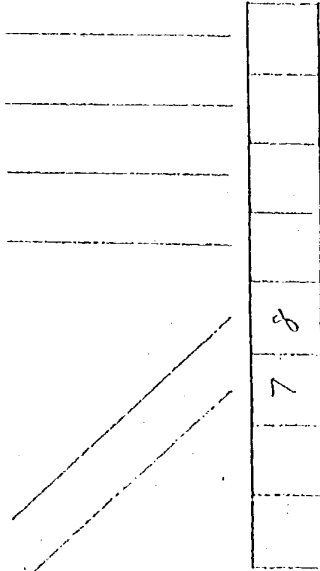
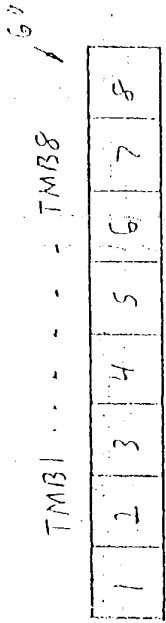
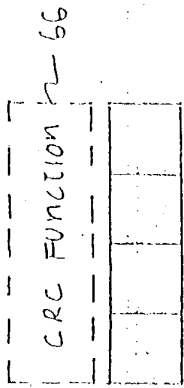


FIG. 2



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Start  
real time processing -60

Acquire, filter  
analog data -62

Acquire contact input  
data from hardware,  
map to other bits -64

Acquire mirrored input  
bits stored by  
receive module,  
map to other bits -66

Perform relay element  
calculations -68

Perform Logic  
Equations -70

Output bits to  
Transmit Module -72

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